



Atty. Docket No.: 3175-Z

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Laung-Terng Wang et al

Serial No. 10/086,214

Examiner James C. Kerveros

Filed: March 27, 2002

Group Art Unit 2184

For: Method and Apparatus for Diagnosing Failures in an Integrated
Circuit Using Design-for-Debug (DFD) Techniques

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed January 27, 2005,
please amend the above-identified application as follows: